FIG. 1

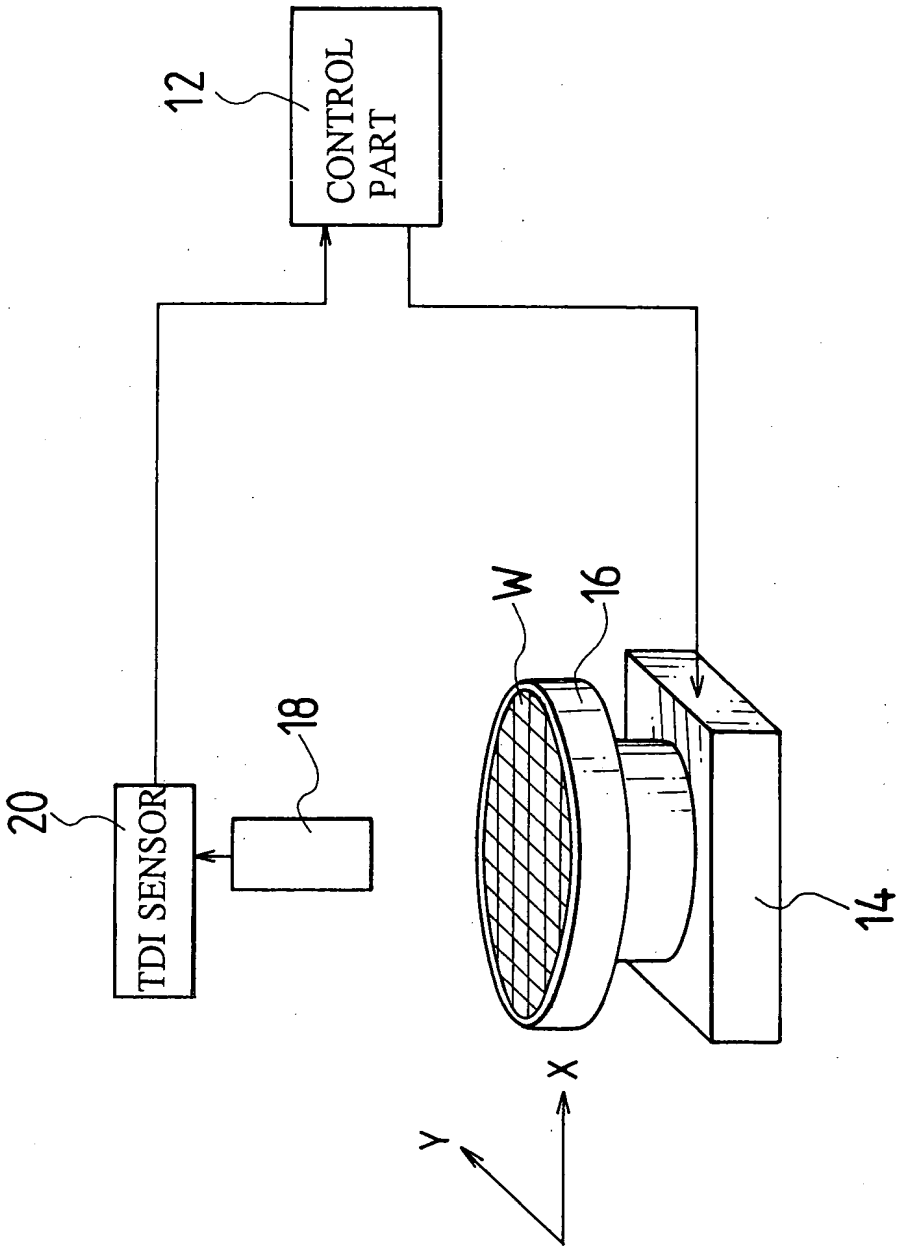

FIG. 2

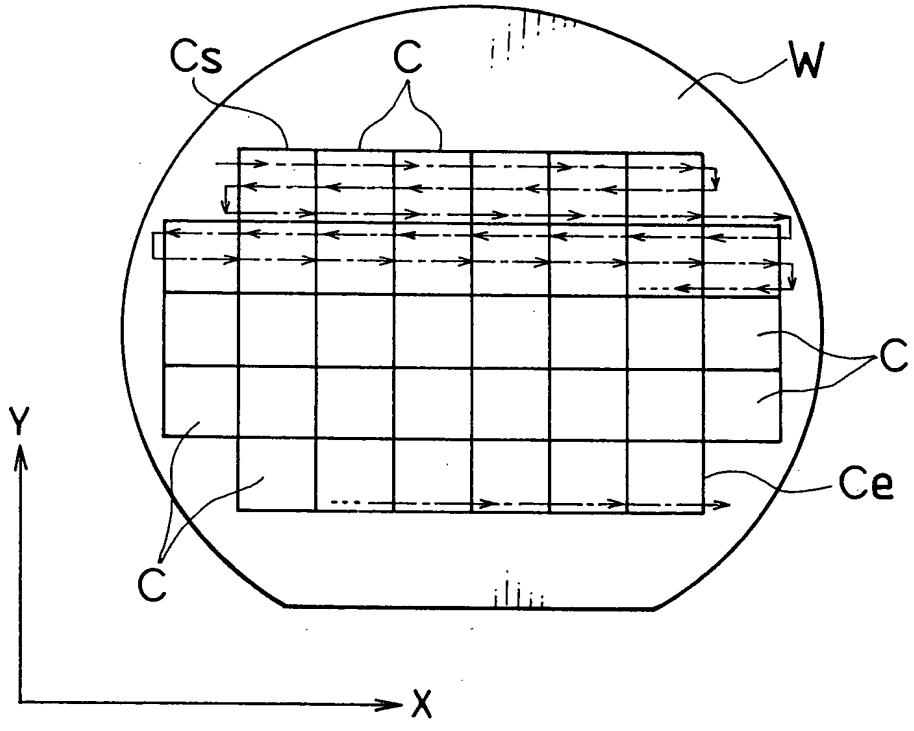

FIG. 3

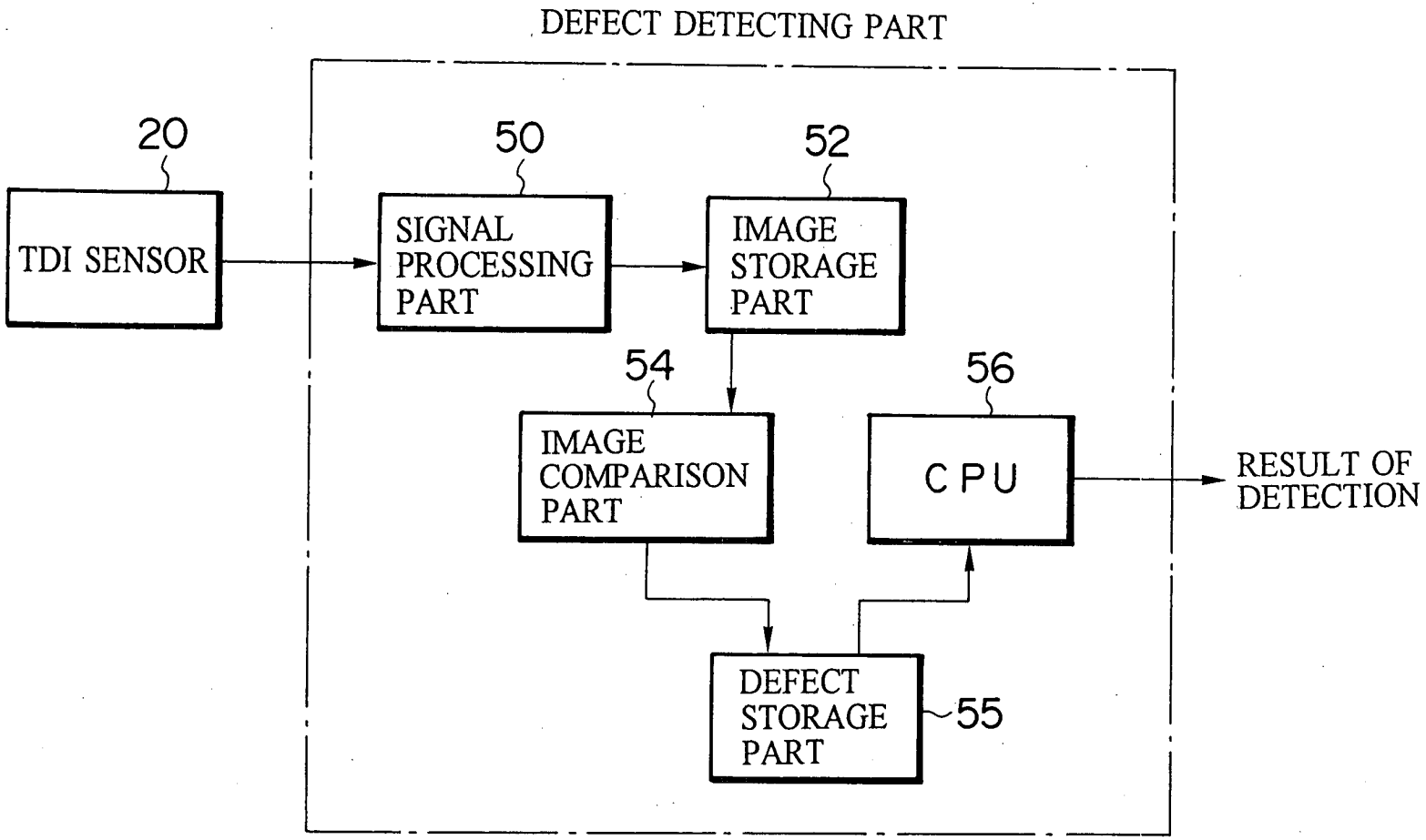
DEFECT DETECTING PART
20
TDI SENSOR
50
SIGNAL PROCESSING PART
52
IMAGE STORAGE PART
54
IMAGE COMPARISON PART
56
CPU
RESULT OF DETECTION
DEFECT STORAGE PART
55

FIG. 4

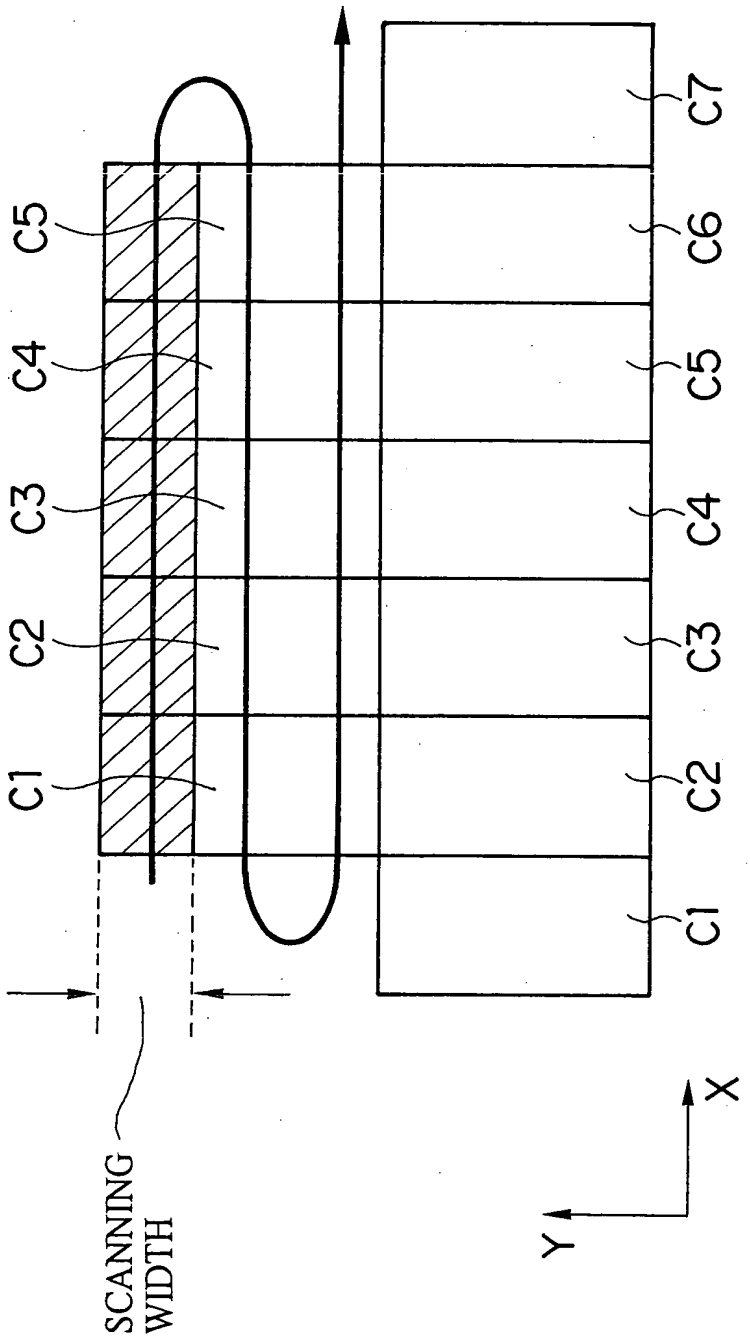

FIG. 5

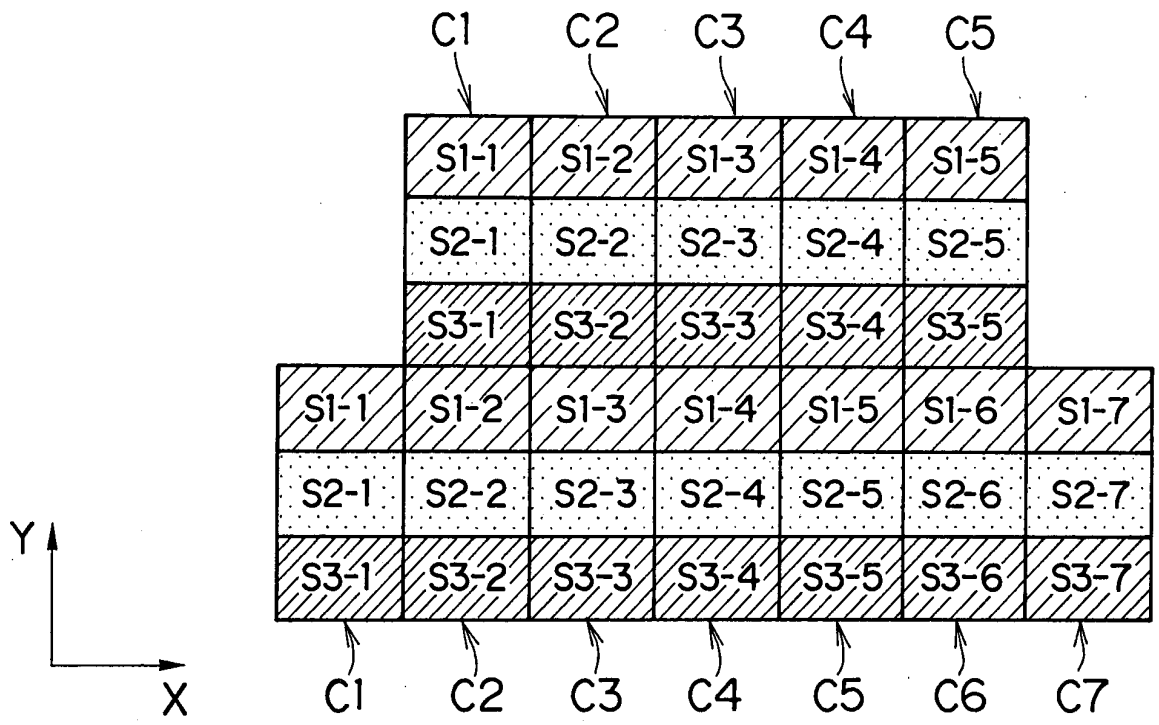
C1
C2
C3
C4
C5
S1-1
S1-2
S1-3
S1-4
S1-5
S2-1
S2-2
S2-3
S2-4
S2-5
S3-1
S3-2
S3-3
S3-4
S3-5
S1-1
S1-2
S1-3
S1-4
S1-5
S1-6
S1-7
S2-1
S2-2
S2-3
S2-4
S2-5
S2-6
S2-7
S3-1
S3-2
S3-3
S3-4
S3-5
S3-6
S3-7
C1
C2
C3
C4
C5
C6
C7
Y
X

FIG. 6

ROW NUMBER	SCANNING NUMBER	CHIP NUMBER OF WHICH IMAGE IS BEING CAPTURED	COMBINATION OF IMAGES SUBJECT TO COMPARISON
1	S1	C1	NO COMPARISON
		C2	S1-1&S1-2
		C3	S1-1&S1-3
		C4	S1-2&S1-4
		C5	S1-3&S1-5
	S2	C5	S1-4&S1-5
		C4	S2-5&S2-4
		C3	S2-5&S2-3
		C2	S2-4&S2-2
		C1	S2-3&S2-1
	S3	C1	S2-2&S2-1
		C2	S3-1&S3-2
		C3	S3-1&S3-3
		C4	S3-2&S3-4
		C5	S3-3&S3-5
2	S1	C7	S3-4&S3-5
		C6	S1-7&S1-6
		C5	S1-7&S1-5
		C4	S1-6&S1-5
		C3	S1-4&S1-3
		C2	S1-4&S1-2
		C1	S1-3&S1-1
	S2	C1	S1-2&S1-1
		C2	S2-1&S2-2
		C3	S2-1&S2-3
		C4	S2-2&S2-3
⋮	⋮	⋮	⋮

FIG. 7 (A)
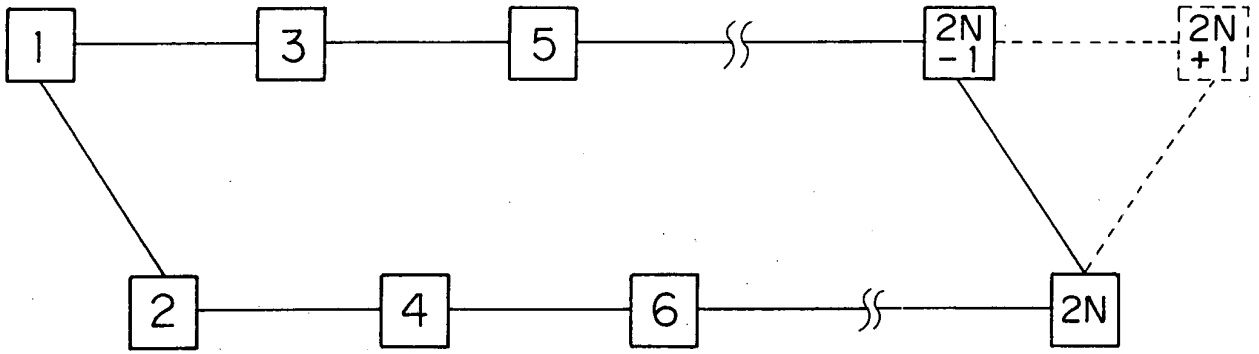
FIG. 7 (B)